

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al. Art Unit : 2171  
 Serial No.: 10/020,815 Examiner : Unknown  
 Filed : December 12, 2001  
 Title : QUEUE MANAGEMENT

Commissioner for Patents  
 Washington, D.C. 20231

TRANSMITTAL LETTER

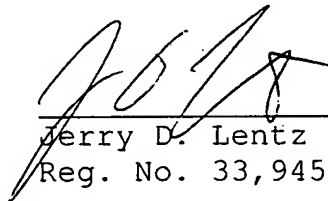
Correspondence relating to this application is enclosed.  
 The required fees are computed below. Please apply any charges  
 not covered, or any credits, to Deposit Account No. 06-1050.

Total						
Claims	31	-	24	=	7	\$126
Independent	4	-	3	=	1	\$84
Request for Approval of Drawing Change and 1 sheet of drawings						\$0
TOTAL FEE DUE						\$210

A check for \$210 is attached.

Respectfully submitted,

Date: 3/21/02

  
 Jerry D. Lentz  
 Reg. No. 33,945

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 Signature Deborah K. Sim  
 Debbrah K. Sim  
 Typed or Printed Name of Person Signing  
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PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

In the specification:

Replace the line at page 7, line 12 with the following rewritten line:

-- process 120 returns (124)[(122)] the BDA from the head of the queue --

Replace the paragraph beginning at page 8, line 11 with the following rewritten paragraph:

-- Referring to FIG. 2C, a logical representation of the sequence of data packets that are transmitted by a system

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performing process 80 is shown. The data buffers, 18a-18n, and cell numbers of FIG. 2C correspond to the same numbers shown in Figs. 1 and 2B. As shown in FIG. 2C, a system performing process 80 causes the two cells of data buffer 18a to be transmitted before the transmission of the first cell of data buffer 18b is begun. Likewise, data buffer 18b completes transmission before the first cell of data buffer 18c begins transmission, and so forth. --

In the claims:

Add claims 25 through 31.

-- 25. (New) A processing system for managing queue entries comprising:

a processor;

a memory to store queue entries; and

a storage-medium accessible by the processor to store executable instructions, which when accessed by the processor causes the processor to:

store addresses in a first queue entry as a linked list, each of the stored addresses including a cell count;  
retrieve a first address from the first queue entry;  
and

modify the linked list of addresses of the first queue entry based on the cell count of the first address retrieved.

26. (New) The system of claim 1, wherein modifying comprises:

decrementing the cell count of the first address each time the first address is retrieved.

27. (New) The method of claim 26, further comprising instructions, which when accessed by the processor causes the processor to:

determine the cell count is zero; and

set a second address as the first address of the first queue entry.

28. (New) The system of claim 27, wherein storing addresses further comprises:

setting the first address as the head address of the first queue entry; and

linking a second address to the first address of the first queue entry.

29. (New) The system of claim 28, wherein linking the second address to the first address further comprises instructions, which when accessed by the processor causes the processor to:

setting the second address as a tail address of the first queue entry.

30. (New) The system of claim 29, further comprises instructions, which when accessed by the processor causes the processor to:

link a third address to the first queue entry by storing the third address in the location indicated by the tail address.

31. (New) The system of claim 29, further comprises instructions, which when accessed by the processor causes the processor to:

increment a queue count each time an address is linked to the first queue entry. --

In the drawings:

Enclosed is one amended page of drawings for the application with changes indicated in red.

REMARKS

Enclosed is one amended page of drawings for the application with changes indicated in red. The drawings are being amended to correct inaccuracies in the original drawings submitted and correspond to the written specification of the original application. The amendment to the drawings does not add new matter, it merely conforms to the written description beginning at page 7, line 17, and continuing through page 8, line 2.

The specification has been amended to correct minor inaccuracies in the description related to FIG. 2C. The amendments to the specification are supported by the original drawing marked FIG. 2C and the description of FIG. 2C in the section entitled: "DESCRIPTION OF THE DRAWINGS".

Claims 25-31 have been added. Claims 1-31 are pending in this application.

Applicant submits that no new matter is introduced and requests approval of the amended drawings and amendments to the specification.


Attached is a marked-up version of the changes being made by the current amendment.

Applicant asks that all claims be examined. Enclosed is a \$210.00 check for excess claim fees. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: \_\_\_\_\_

3/21/02

  
\_\_\_\_\_  
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Version with markings to show changes made

In the specification:

Line at page 7, line 12 has been amended as follows:

process 120 returns (124)[(122)] the BDA from the head of the queue

The paragraph beginning at page 8, line 11 has been amended as follows:

Referring to FIG. 2C [4], a logical representation of the sequence of data packets that are transmitted by a system performing process 80 is shown. The data buffers, 18a-18n, and cell [Cell] numbers of FIG. 2C [4] correspond to the same numbers shown in Figs. 1 and 2B. As shown in FIG. 2C [4], a system performing process 80 causes the two cells of data buffer 18a to be transmitted before the transmission of the first cell [Cell] of data buffer 18b is begun. Likewise, data buffer 18b completes transmission before the first cell of data buffer 18c begins transmission, and so forth.

In the claims:

Claims 25 through 31 have been added.





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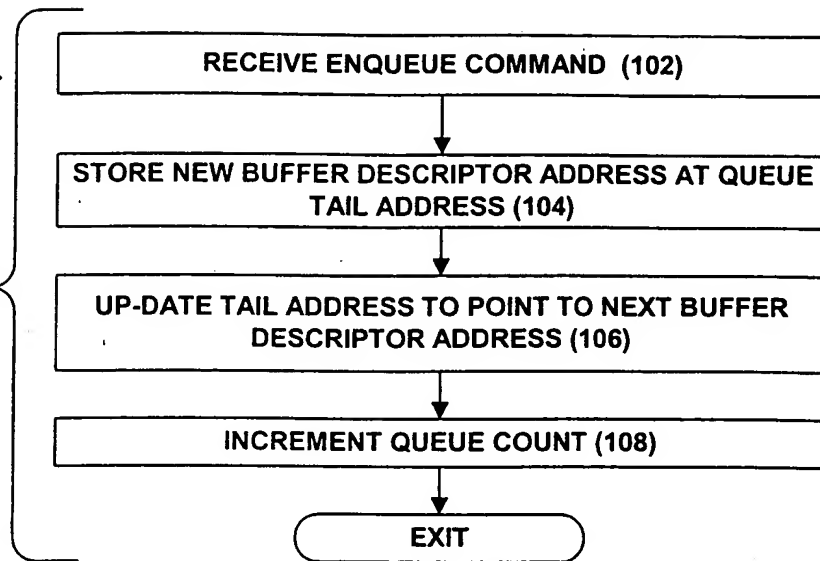


FIGURE 3A

120

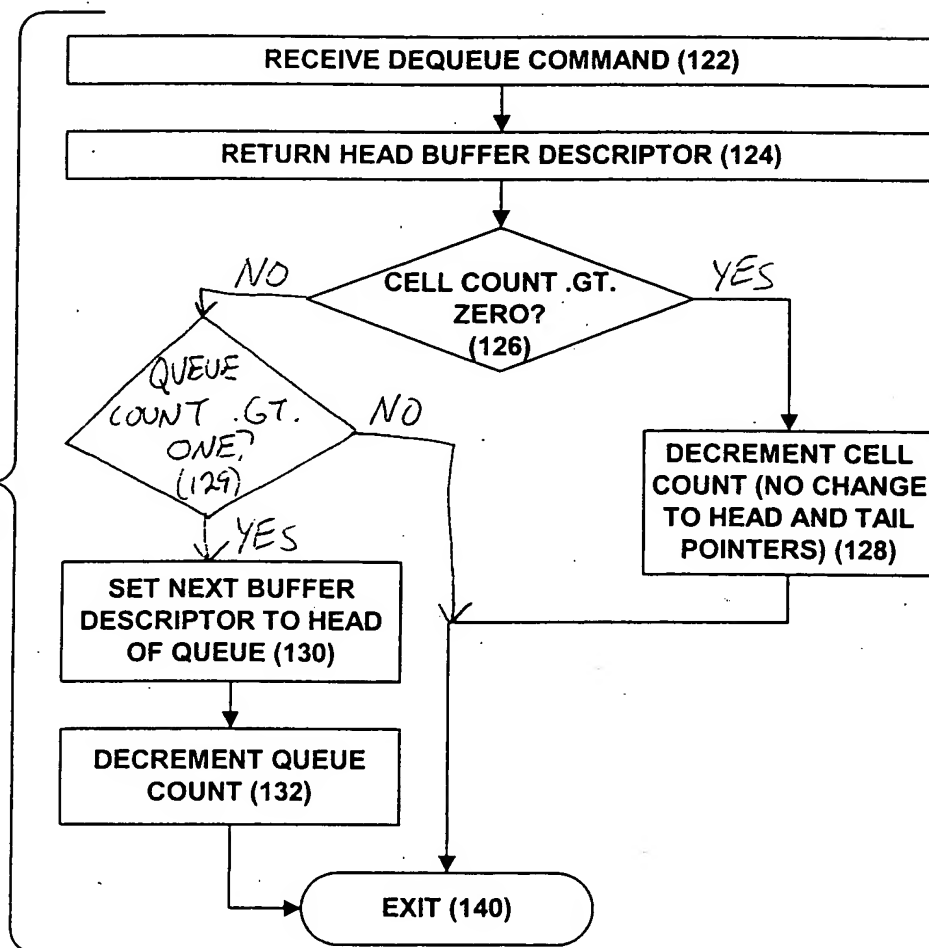


FIGURE 3B



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Attention: Official Draftsman  
Commissioner for Patents  
Washington, D.C. 20231

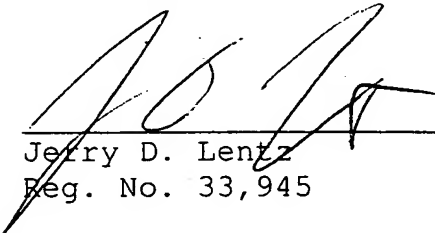
REQUEST FOR APPROVAL OF DRAWING CHANGES

Enclosed is one page of amended drawings for the application with changes indicated in red.

Applicant submits that no new matter is introduced and requests approval of the amended drawings.

Respectfully submitted,

Date: 3/21/02

  
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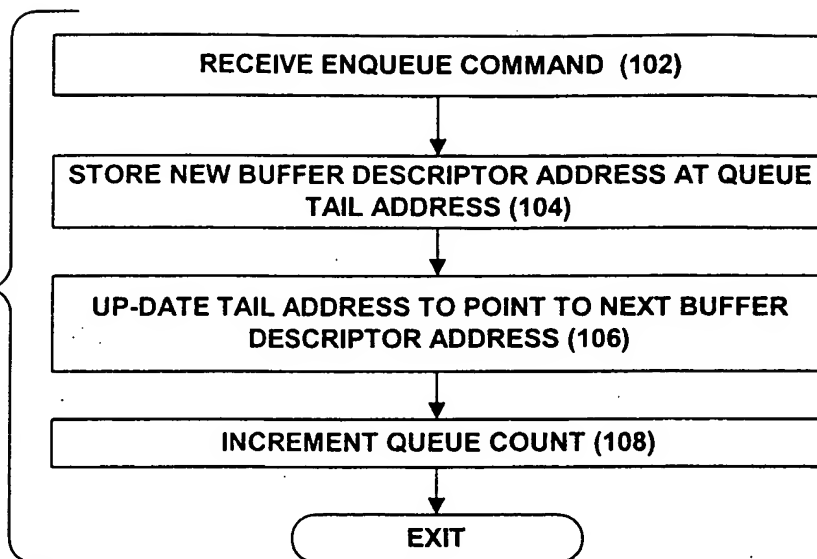


FIGURE 3A

120

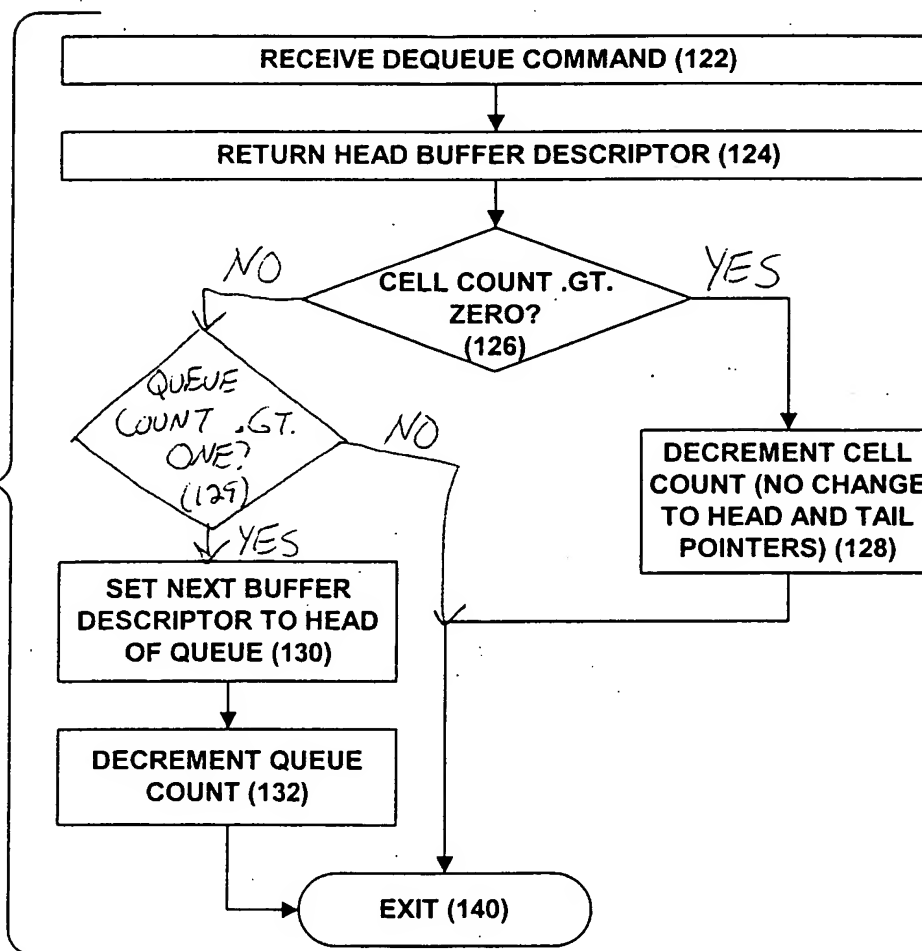


FIGURE 3B



## PATENT

ATTORNEY DOCKET NO.:

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The Patent and Trademark Office date stamp sets forth the date of receipt of:

Applicant or Patentee

Gilbert Walrich et al.

No. (Application, Appeal, Interference, Patent, Reexam)

10/020,815

Filing or Issue Date

December 12, 2001

Title:

QUEUE MANAGEMENT

- ☒ Transmittal Letter (2 Copies) ☐ With Pet. for Ext. (1 Pg)  
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☐ Combined Declaration and Power of Attorney  
☐ Small Entity Statement  
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